

Description

SL1117A of positive adjustable and fixed regulators is designed to provide 1.0A output with low dropout voltage performance. On-chip trimming adjusts the reference voltage to 1%. For usage on working in post regulators or microprocessor power supplies, low voltage operation and fast transient response are required.

Pin-to-pin compatible with the LT1086 family of regulators, SL1117A is available in surface-mount SOT-89 package.

Features

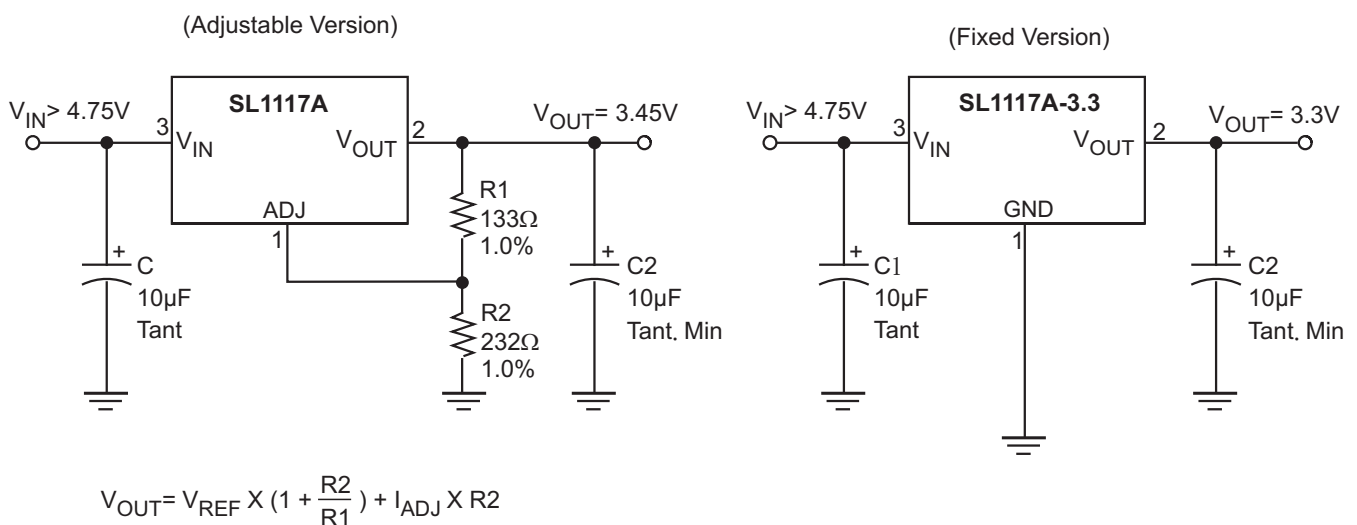
- ◆ Adjustable or Fixed Output
- ◆ Output Current of 1.0A
- ◆ Dropout Voltage (Typical) 1.3V @ 1.0A
- ◆ Line Regulation 0.2% max.
- ◆ Load Regulation 0.4% max.
- ◆ Fast Transient Response
- ◆ Current Limit Protection
- ◆ Thermal Shutdown Protection

Application

High Efficiency Linear Regulators
 Post Regulators for Switching Supplies
 Microprocessor Supply

Hard Drive Controllers
 Battery Chargers
 Adjustable Power Supply

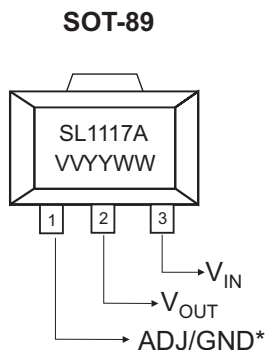
TYPICAL APPLICATION CIRCUITS



Notes:

1. C1 needed if device is far from filter capacitors
2. C2 minimum value required for stability

◆ **MARKING INFORMATION & PIN CONFIGURATIONS** (Top View)



V V/VVV = Output Voltage (50 = 5.0V, 285= 2.85V ,A =Adj)
 YY = Year
 W W = Weekly

* On fixed versions Pin 1 = GND,
 on adjustable versions Pin 1 = ADJ
 * Tab = V_{OUT}

◆ **ORDERING INFORMATION** (Green Package Products are available now!)

| Ordering Number | Output Voltage | Package | Shipping |
|--------------------|----------------|---------|---------------------------|
| SL 1117A-AST89R | Adj | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-1.5ST89R | 1.5 | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-1.8ST89R | 1.8 | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-2.5ST89R | 2.5 | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-2.85ST89R | 2.85 | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-3.0ST89R | 3.0 | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-3.3ST89R | 3.3 | SOT-89 | 1,000 Units / Tape & Reel |
| SL 1117A-5.0ST89R | 5.0 | SOT-89 | 1,000 Units / Tape & Reel |

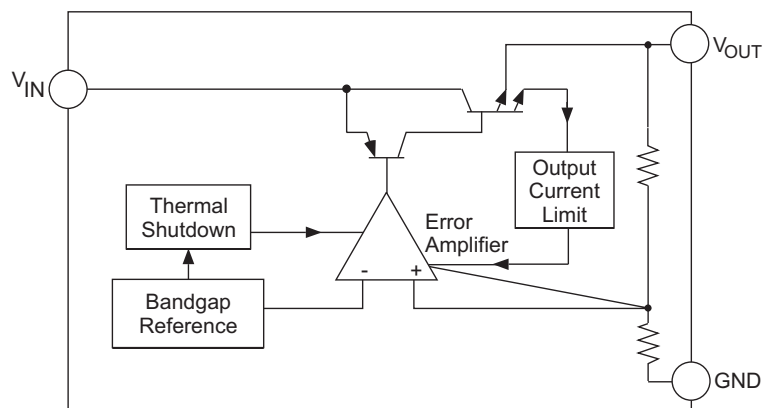
* For detail ordering number identification, please see last page.

◆ ABSOLUTE MAXIMUM RATINGS

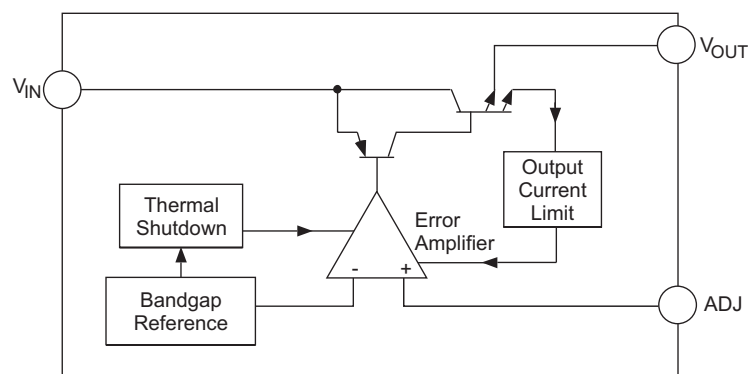
| PARAMETER | SYMBOL | VALUE | UNIT |
|--|-----------------|--------------------------|------|
| Power Dissipation | P_D | Internally limited | W |
| Input Voltage | V_{IN} | 15.0 | V |
| Lead Temperature(Soldering, 10sec) | T_{LEAD} | 300 | °C |
| Operating Junction Temperature Range Control Section Power Transistor | T_J | -40 to 125 -40 to 150 | °C |
| Storage Temperature Range | T_{STG} | -65 to + 150 | °C |
| Thermal Characteristics SOT-223 Thermal Resistance, Junction-to-Case DPAK Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 15 6.0 | °C/W |
| Continuous Total Power Dissipation SOT - 89 | P_D | 500 | mW |

◆ BLOCK DIAGRAM

(Fixed Version)



(Adjustable Version)



◆ ELECTRICAL CHARACTERISTICS

(Typicals and limits appearing in normal type apply for $T_J = 25^\circ\text{C}$)

| Parameter | | Symbol | Condition | Min | Typ | Max | Unit |
|---------------------------|--------------------|--------------|---|----------------|----------------|----------------|---------------------|
| Reference Voltage | SL1117A-Adj | V_{REF} | $I_{OUT}=10\text{mA}, V_{IN}=5\text{V}$ $I_{OUT} = 1\text{mA to } 1\text{A}, V_{IN}-V_{IN}=1.5\text{V to } 10\text{V}$ | 1.238 1.225 | 1.250 1.250 | 1.262 1.275 | V |
| Output Voltage | | | $I_{OUT}=10\text{mA}, V_{IN}=V_{OUT}+1.5\text{V}$ | -1 | | +1 | % |
| | | | $V_{IN}=V_{OUT}+1.5\text{V to } 10\text{V}$ $0 \leq I_{OUT} \leq 1.0\text{A}$ | -2 | | +2 | % |
| Line Regulation | All | REG_{LINE} | $I_{Load}=10\text{mA},$ $V_{IN}-V_{IN}=1.5\text{V to } 10\text{V}$ | | 0.04 | 0.20 | % |
| Load Regulation | All | REG_{LOAD} | $V_{IN}=V_{OUT} + 1.5\text{V}$ $I_{Load}=10\text{mA to } 1.0\text{A}$ | | 0.20 | 0.40 | % |
| Dropout Voltage | All | V_D | $I_{OUT}=1.0\text{A}$ | | 1.3 | 1.5 | V |
| Current Limit | All | I_{CL} | $V_{IN}-V_{OUT}=1.5\text{V}$ | 1.0 | 1.4 | | A |
| Minimum Load Current | SL1117A-Adj | $I_{O\ MIN}$ | $V_{IN}=5\text{V}, V_{adj}=0\text{V}$ | | 3 | 7 | mA |
| Ground Current | All Fixed Versions | I_Q | $V_{IN}=V_{OUT} + 1.5\text{V}$ $I_{Load}=10\text{mA to } 1.0\text{A}$ | | 7 | 10 | mA |
| Adjust Pin Current | SL1117A-Adj | I_{ADJ} | $I_{Load}=10\text{mA}, 2.65\text{V} \leq V_{IN} \leq 7\text{V}$ | | 40 | 90 | μA |
| Temp. Coefficient | All | | $V_{IN}-V_{OUT}=1.5\text{V}, I_{Load}=10\text{mA}$ | | 0.005 | | $\%/^\circ\text{C}$ |
| Thermal Regulation | All | T_C | $T_A=25^\circ\text{C}, 30\text{ms pulse}$ | | 0.003 | | $\%/W$ |
| Ripple Rejection (Note 1) | All | R_A | $V_{IN}-V_{OUT}=1.5\text{V}, I_{Load}=1.0\text{A}$ | 60 | 65 | | dB |

Note 1: 120Hz input ripple (C_{ADJ} for ADJ = 25 μF)

◆ APPLICATION INFORMATION

SL1117A linear regulators provide fixed and adjustable output voltages at currents up to 1.0A. These regulators are protected against overcurrent conditions and include thermal shutdown protection. The SL1117A's have a composite PNP-NPN output transistor and require an output capacitor for stability. A detailed procedure for selecting this capacitor follows.

Adjustable Operation

The SL1117A has an output voltage range of 1.25 V to 5.5 V. An external resistor divider sets the output voltage as shown in Figure 1. The regulator maintains a fixed 1.25V (typical) reference between the output pin and the adjust pin.

A resistor divider network R1 and R2 causes a fixed current to flow to ground. This current creates a voltage across R2 that adds to the 1.25V across R1 and sets the overall output voltage. The adjust pin current (typically 35 μ A) also flows through R2 and adds a small error that should be taken into account if precise adjustment of V_{OUT} is necessary.

The output voltage is set according to the formula:

$$V_{OUT} = V_{REF} \times \left(\frac{R1 + R2}{R1} \right) + I_{Adj} \times R2$$

The term $I_{Adj} R2$ represents the error added by the adjust pin current. R1 is chosen so that the minimum load current is at least 2.0mA. R1 and R2 should be the same type, e.g. metal film for best tracking over temperature. While not required, a bypass capacitor from the adjust pin to ground will improve ripple rejection and transient response. A 0.1 μ F tantalum capacitor is recommended for first cut design. Type and value may be varied to obtain optimum performance vs. price.

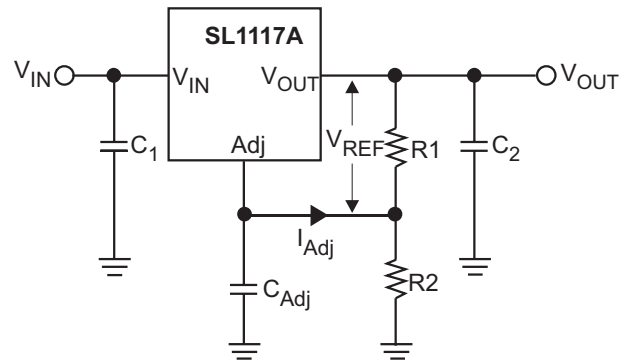


Figure 1. Resistor Divider Scheme

Stability Considerations

The output compensation capacitor helps to determine three main characteristics of a linear regulator's performance: start-up delay, load transient response, and loop stability. The capacitor value and type is based on cost, availability, size and temperature constraints. A tantalum or aluminum electrolytic capacitor is preferred, as a film or ceramic capacitor with almost zero ESR can cause instability. An aluminum electrolytic capacitor is the least expensive type, but when the circuit operates at low temperatures, both the value and ESR of the capacitor will vary widely. For optimum performance over the full operating temperature range, a tantalum capacitor is best. A 22 μ F tantalum capacitor will work fine in most applications, but with high current regulators such as the SL1117A higher capacitance values will improve the transient response and stability. Most applications for the SL1117A's involve large changes in load current, so the output capacitor must supply instantaneous load current. The ESR of the output capacitor causes an immediate drop in output voltage given by:

$$\Delta V = \Delta I \times ESR$$

In microprocessor applications an output capacitor network of several tantalum and ceramic capacitors in parallel is commonly used. This reduces overall ESR and minimizes the instantaneous output voltage drop under transient load conditions. The output capacitor network should be placed as close to the load as possible for the best results.

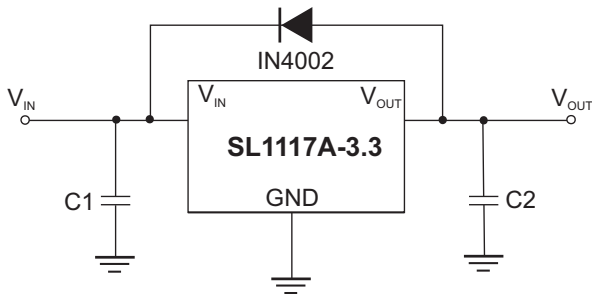
Used with large output capacitance values and the input voltage is instantaneously shorted to ground, damage can occur. In this case, a diode connected as shown above in Figure 1.

Protection Diodes

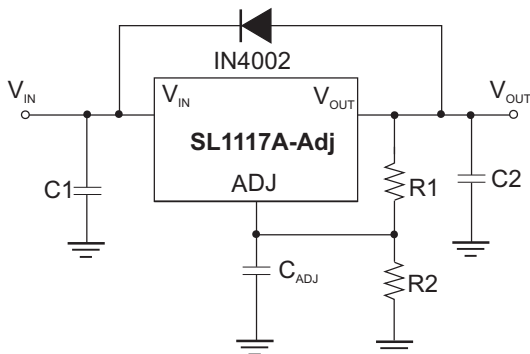
When large external capacitors are used with most linear regulator, it is wise to add protection diodes. If the input voltage of the regulator is shorted, the output capacitor will discharge into the output of the regulator. The discharge current depends on the value of capacitor, output voltage, and rate at which V_{IN} drops.

Figure 2^{(a),(b)} Protection Diode Scheme for Large Output Capacitors

(a) Fixed Version



(b) Adjustable Version



In the SL1117A linear regulators, the discharge path is through a large junction, and protection diodes are normally not needed. However, damage can occur if the regulator is used with large output capacitance values and the input voltage is instantaneously shorted to ground. In this case, a diode connected as shown above in Figure 2.

Output Voltage Sensing

The SL1117A are three terminal regulators. For which, they cannot provide true remote load sensing. Load regulation is limited by the resistance of the conductors connecting the regulator to the load. For best results the SL1117A should be connected are as shown in Figure 3.

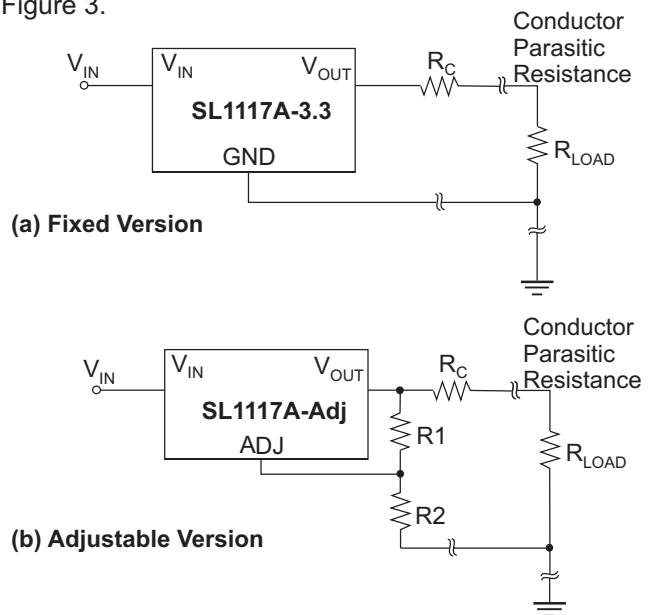


Figure 3^{(a),(b)} Conductor Parasitic Resistance Effects are Minimized by this Grounding Scheme For Fixed and Adjustable Output Regulators

Calculating Power Dissipation and Heat Sink Requirements

The SL1117A precision linear regulators include thermal shutdown and current limit circuitry to protect the devices. However, high power regulators normally operate at high junction temperatures. It is important to calculate the power dissipation and junction temperatures accurately to be sure that you use an adequate heat sink. The case is connected to V_{OUT} on the SL1117A, and electrical isolation may be required for some applications. Thermal compound should always be used with high current regulators like the SL1117A.

The thermal characteristics of an IC depend four factors:

1. Maximum Ambient Temperature $T_A(^{\circ}\text{C})$
2. Power Dissipation P_D (Watts)
3. Maximum Junction Temperature $T_J(^{\circ}\text{C})$
4. Thermal Resistance Junction to ambient $R_{\theta JA}$ ($^{\circ}\text{C}/\text{W}$)

The relationship of these four factors is expressed by equation (1):

$$T_J = T_A + P_D \times R_{\theta JA} \dots\dots(1)$$

Maximum ambient temperature and power dissipation are determined by the design while the maximum junction temperature and thermal resistance depend on the manufacturer and the package type.

The maximum power dissipation for a regulator is expressed by equation (2):

$$P_{D(\text{max})} = \{ V_{IN(\text{max})} - V_{OUT(\text{min})} \} I_{OUT(\text{max})} + V_{IN(\text{max})} I_Q \dots\dots(2)$$

where:

$V_{IN(\text{max})}$ is the maximum input voltage,

$V_{OUT(\text{min})}$ is the minimum output voltage,

$I_{OUT(\text{max})}$ is the maximum output current

I_Q is the maximum quiescent current at $I_{OUT(\text{max})}$.

A heat sink effectively increases the surface area of the package to improve the flow of heat away from the IC into the air. Each material in the heat flow path between the IC and the environment has a thermal resistance. Like series electrical resistances, these re-

sistance are summed to determine $R_{\theta JA}$, the total thermal resistance between the junction and the air. This is expressed by equation (3):

$$R_{\theta JA} = R_{\theta JC} + R_{\theta CS} + R_{\theta SA} \dots\dots(3)$$

Where all of the following are in $^{\circ}\text{C}/\text{W}$:

$R_{\theta JC}$ is thermal resistance of junction to case,

$R_{\theta CS}$ is thermal resistance of case to heat sink,

$R_{\theta SA}$ is thermal resistance of heat sink to ambient air

The value for $R_{\theta JA}$ is calculated using equation (3) and the result can be substituted in equation (1). The value for $R_{\theta JC}$ is $3.5^{\circ}\text{C}/\text{W}$ for a given package type based on an average die size. For a high current regulator such as the SL1117A the majority of the heat is generated in the power transistor section.

◆ Typical Performance Characteristics

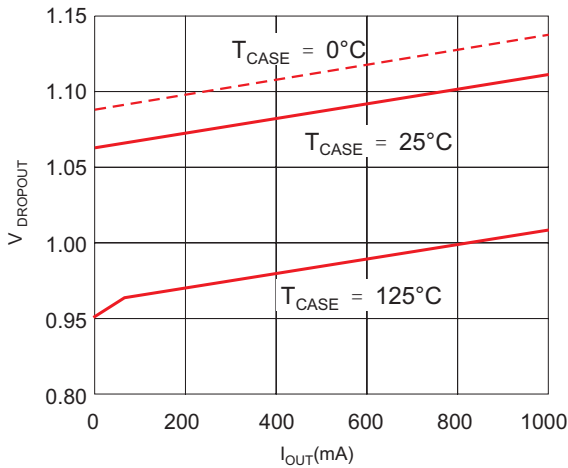


Figure 3. Dropout Voltage vs. Output Current

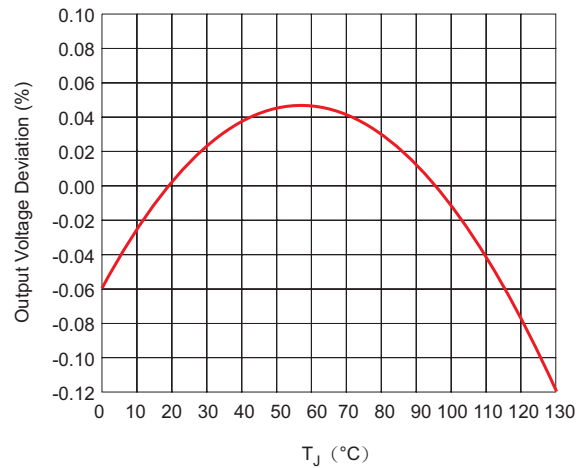


Figure 4. Reference Voltage vs. Temperature

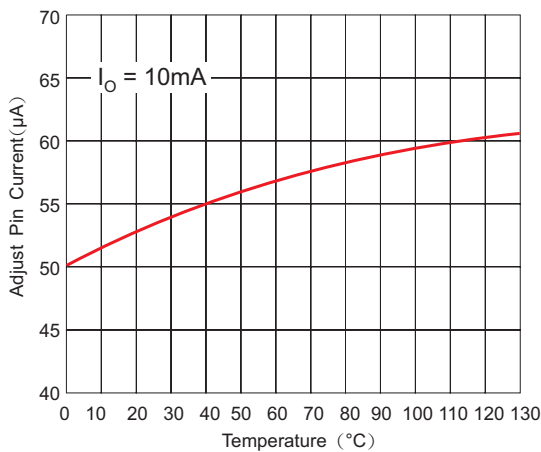


Figure 5. Adjust Pin Current vs. Temperature

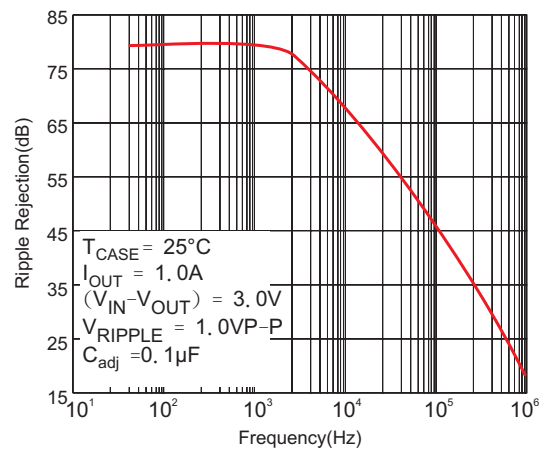


Figure 6. Ripple Rejection vs. Frequency

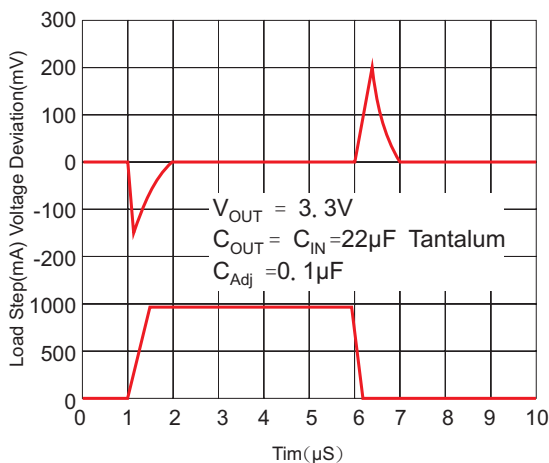


Figure 7. Transient Response

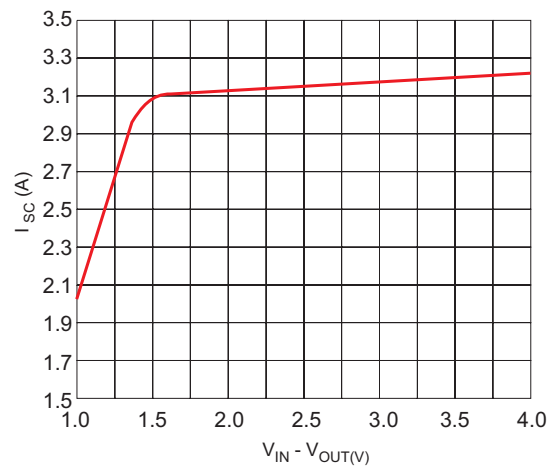
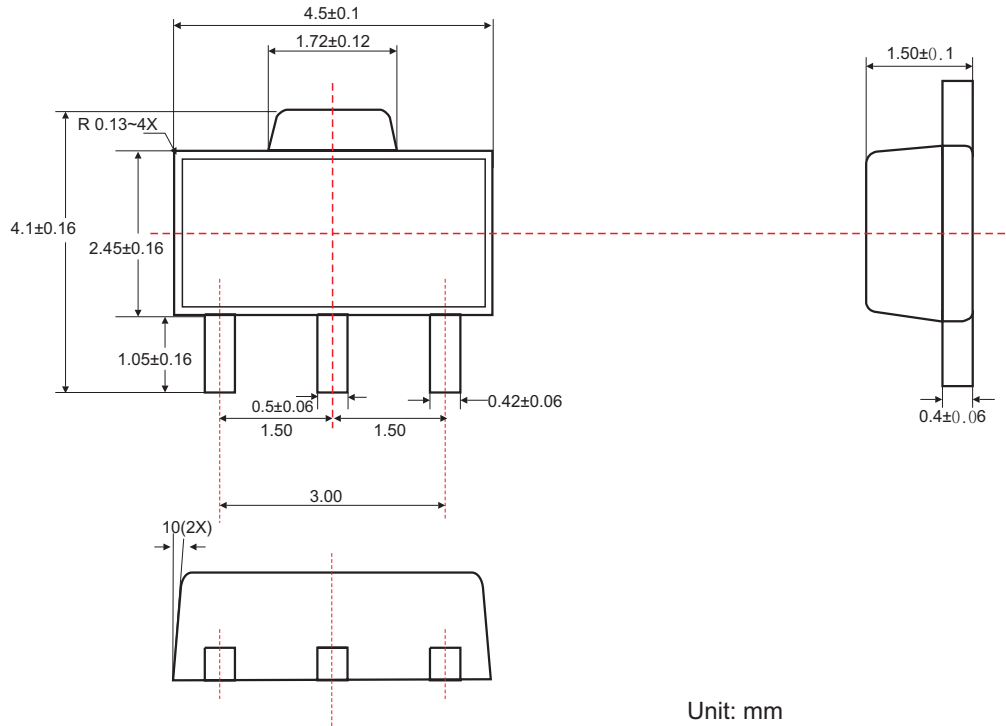


Figure 8. Short Circuit Current vs. $V_{IN} - V_{OUT}$

◆ SOT-89 PACKAGE OUTLINE DIMENSIONS



◆ ORDERING NUMBER

SL1117A A

Circuit Type ←

Output Voltage ←